

23. (New) A liquid crystal display device according to claim 22, wherein the first lower electrode is wider than the first upper electrode.

24. (New) A liquid crystal display device according to claim 23, wherein the second lower electrode is wider than the third upper electrode.

25. (New) A liquid crystal display device according to claim 24, wherein the first space formed between the first lower electrode and the second lower electrode is wider than the second upper electrode, so that a second space is defined between an edge of the first lower electrode and a first edge of the second upper electrode and a third space is defined between an edge of the second lower electrode and a second edge of the second upper electrode.

26. (New) A liquid crystal display device according to claim 25, wherein the lower electrodes are made of transparent conductive material.

27. (New) A liquid crystal display device according to claim 26, wherein the plurality of upper electrode are made of transparent conductive material.

28. (New) A liquid crystal display device according to claim 27, wherein the plurality of upper electrode and the plurality of lower electrode are arranged in parallel.

29. (New) A liquid crystal display device according to claim 28, wherein the first, second and third upper electrodes are connected to each other in each pixel, and the first and second lower electrodes are connected to each other in each pixel.

30. (New) A liquid crystal display device according to claim 29, wherein a distance between the first lower electrode and the second upper electrode along a given direction is smaller than a width of the first lower electrode along the given direction.

31. (New) A liquid crystal display device according to claim 30, wherein a distance between the first lower electrode and the second upper electrode along the given direction is smaller than a width of the second upper electrode along the given direction.

32. (New) A liquid crystal display device comprising:
first and second substrates with a liquid crystal layer provided therebetween;
a plurality of lower electrodes and a plurality of upper electrodes formed
overlying the first substrate, the upper electrode having an upper electrode of first type and an
upper electrode of second type, the upper electrode of first type having a portion that overlaps
with at least one lower electrode, the upper electrode of second type being provided directly
above a space defined between two lower electrodes and not overlapping the two lower
electrodes;
an insulating layer formed between the upper electrodes and the lower electrodes
to electrically isolate the upper and lower electrodes;
wherein the lower electrodes and the upper electrodes define a first region and a
second region,
wherein the first region is defined by at least one lower electrode and the upper
electrode of first type, electric field being formed in the first region by the at least one lower
electrode and the upper electrode of first type,
wherein the second region is defined by the at least one lower electrode and the
upper electrode of second type, electric field being formed in the second region by the at least
one lower electrode and the upper electrode of second type.
33. (New) A liquid crystal display device according to claim 32, wherein the
lower electrodes are wider than the upper electrodes along a given direction.
34. (New) A liquid crystal display device according to claim 33, wherein the
first region is wider than the second region along the given direction.
35. (New) A liquid crystal display device according to claim 34, wherein a
number of the upper electrodes assigned to a given pixel is more than a number of the lower
electrodes assigned to the given pixel.
36. (New) A liquid crystal display device according to claim 35, wherein the
lower electrodes comprise transparent conductive material.

37. (New) A liquid crystal display device according to claim 36, wherein the upper electrodes comprise transparent conductive material.

38. (New) A liquid crystal display device according to claim 37, wherein the upper electrode and the lower electrode are arranged in parallel.